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JTAG Wikipedia

December 8th, 2018 - JTAG named after the Joint Test Action Group which codified it is an industry standard for verifying designs and testing printed circuit boards after manufacture JTAG implements standards for on chip instrumentation in electronic design automation EDA as a complementary tool to digital simulation It specifies the use of a dedicated debug port implementing a serial communications

Field programmable gate array Wikipedia

December 7th, 2018 - A field programmable gate array FPGA is an integrated circuit designed to be configured by a customer or a designer after manufacturing " hence the term field programmable The FPGA configuration is generally specified using a hardware description language HDL similar to that used for an application specific integrated circuit ASIC Circuit diagrams were previously used to specify

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